

FIG. 1
(PRIOR ART)

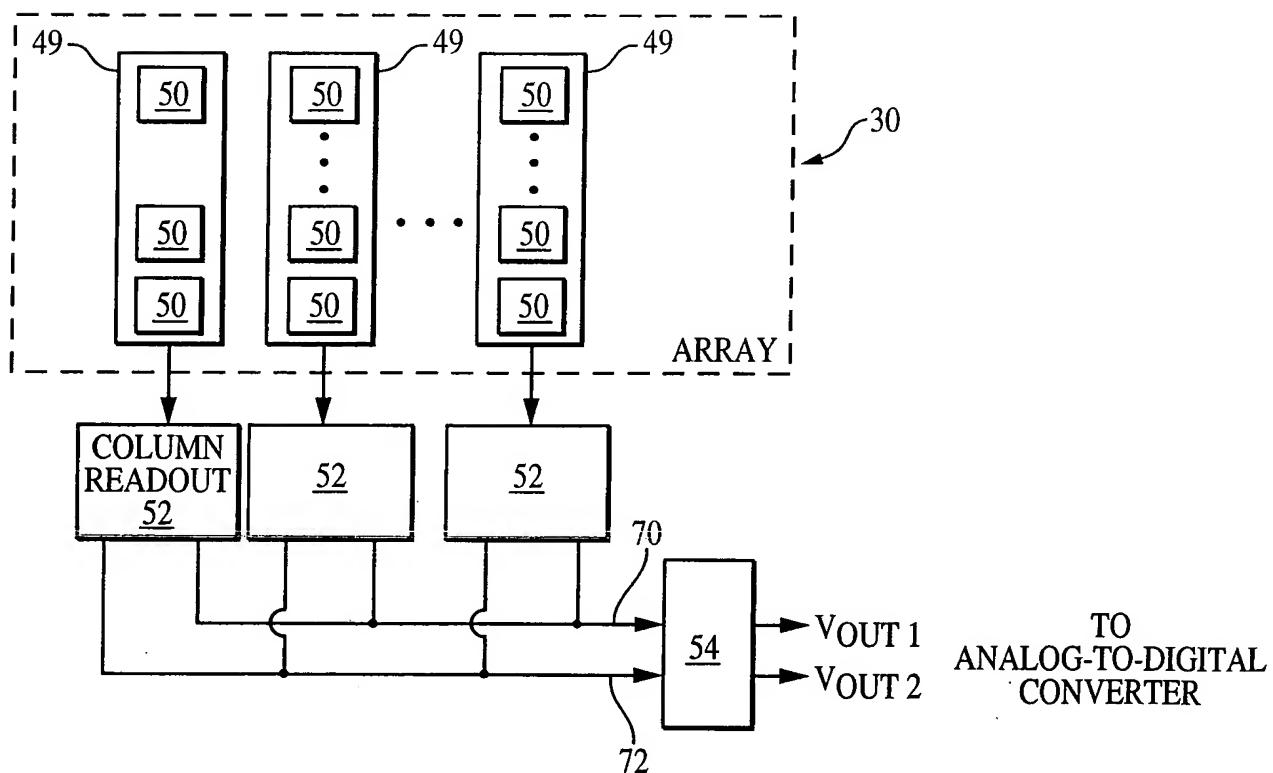


FIG. 2
(PRIOR ART)

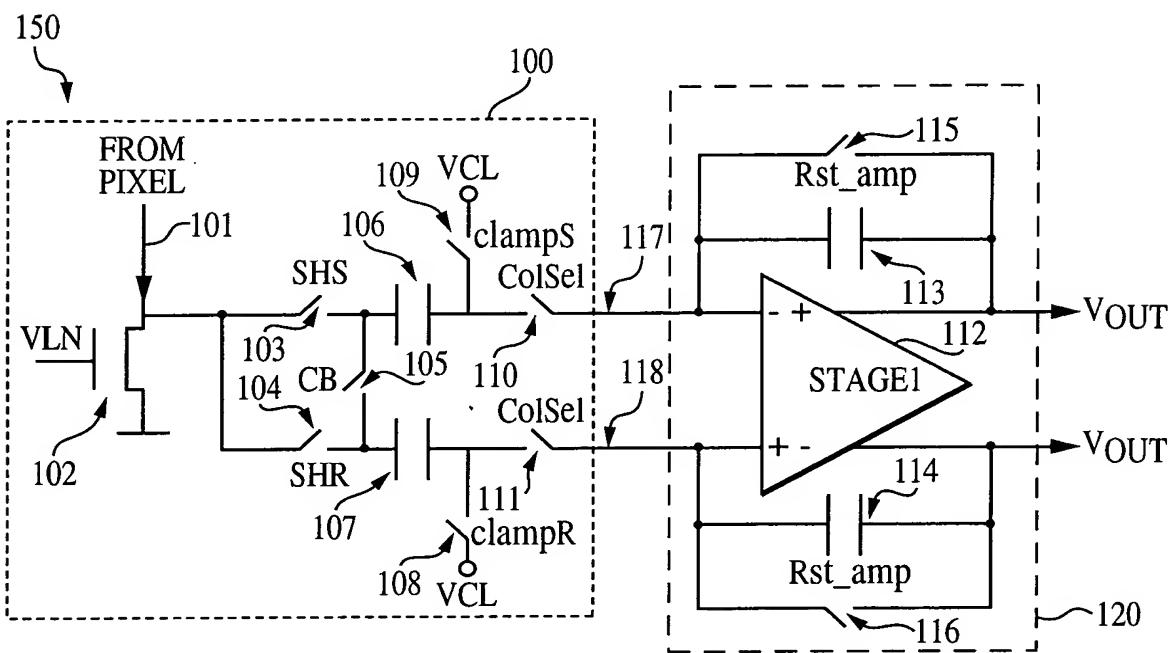


FIG. 3
(PRIOR ART)

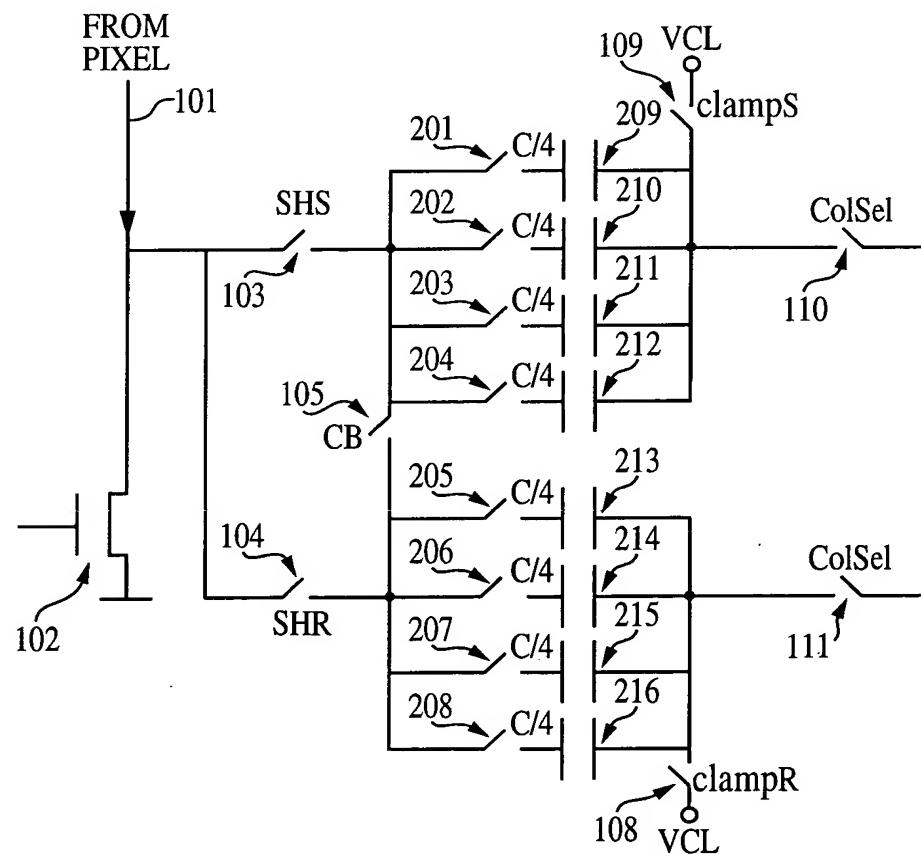


FIG. 4

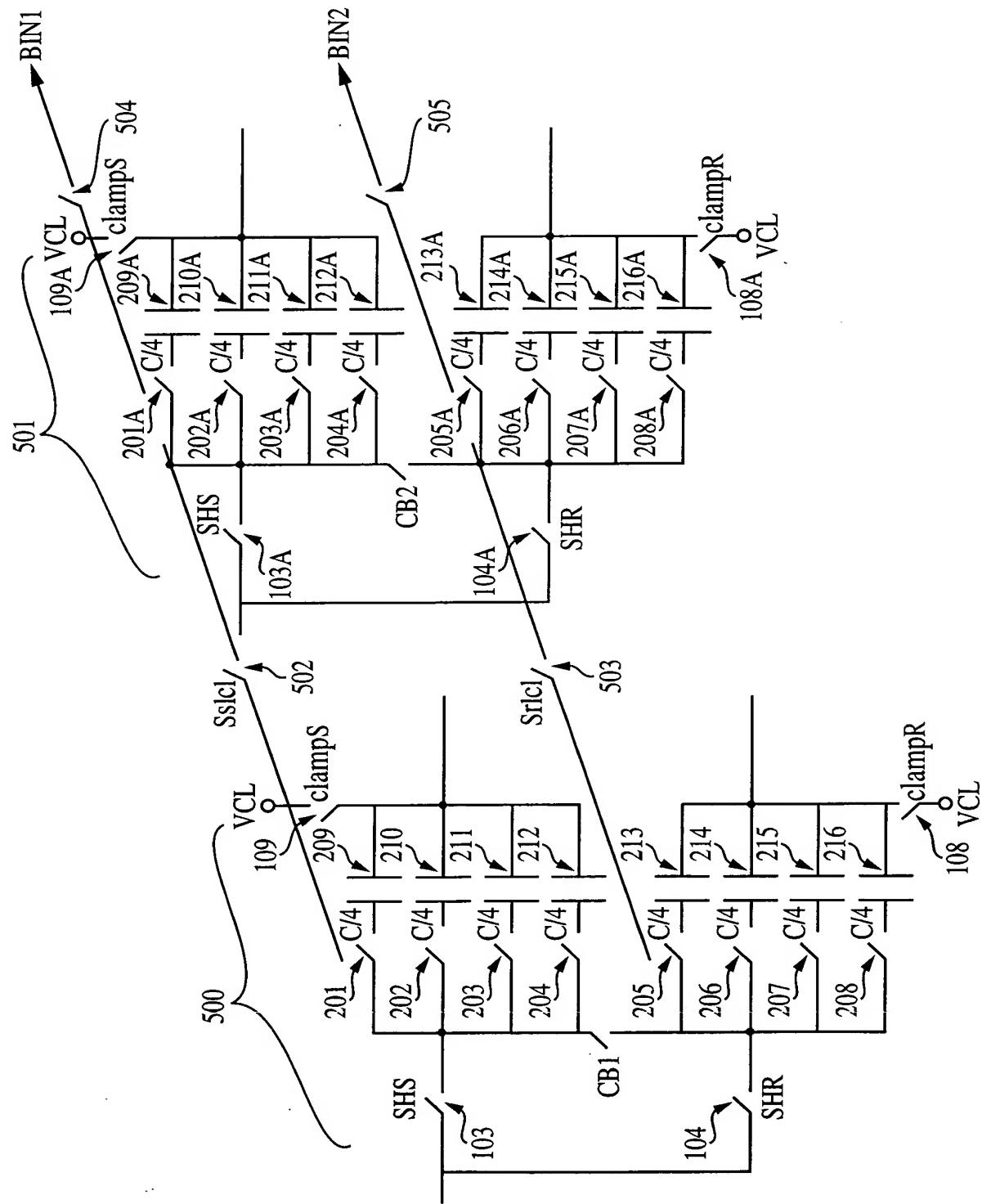


FIG. 5

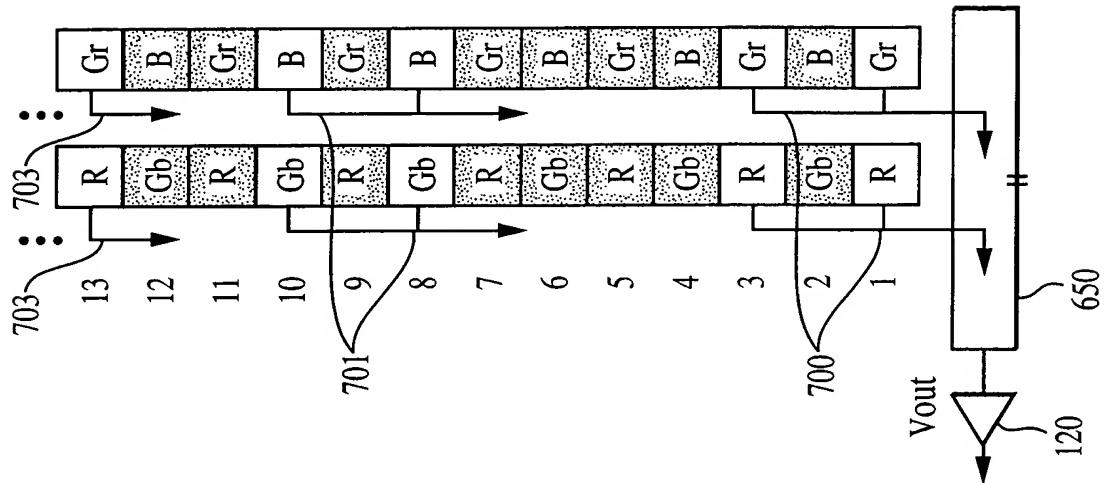


FIG. 7

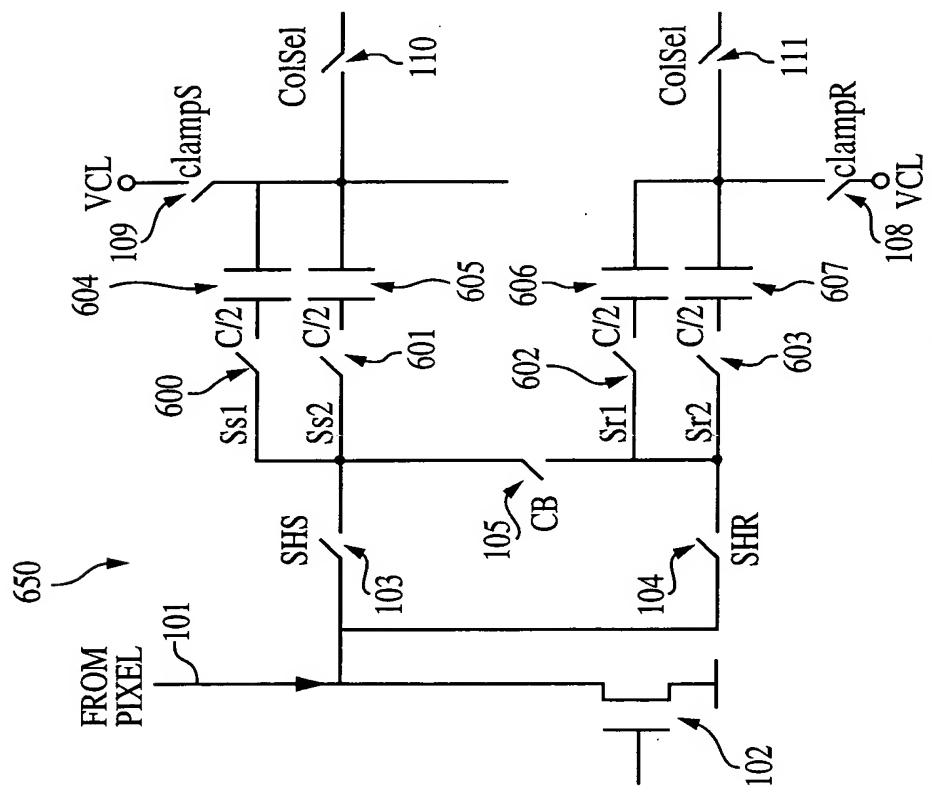


FIG. 6

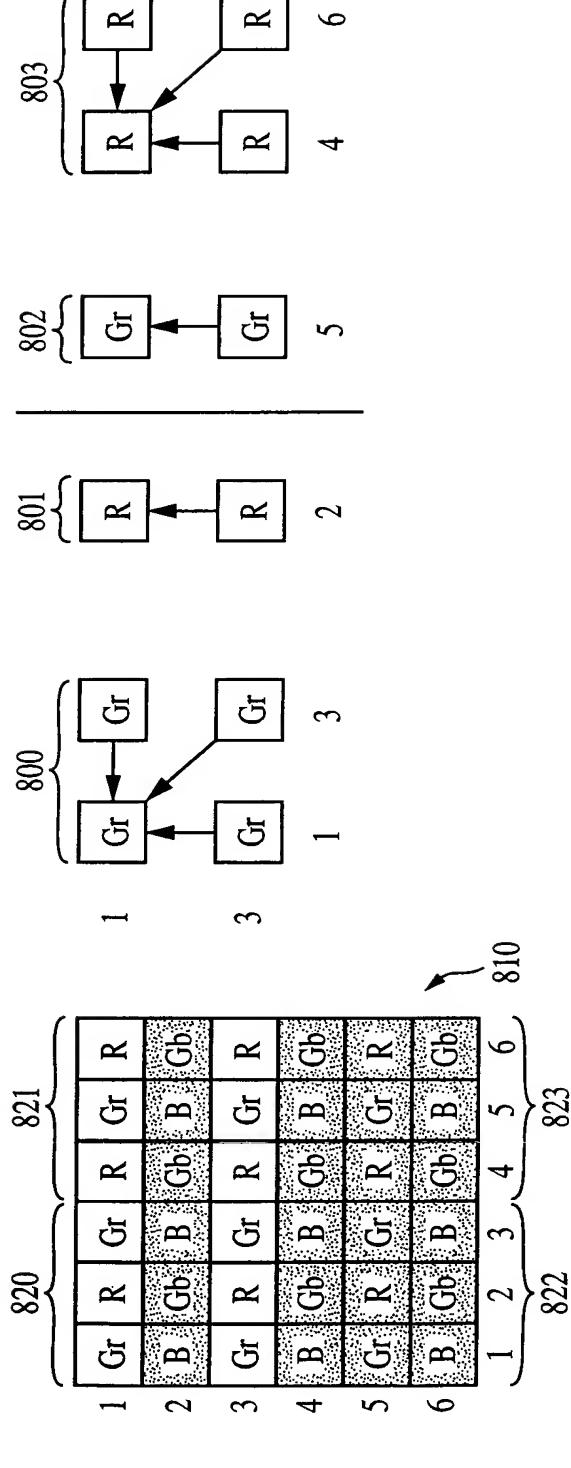


FIG. 8

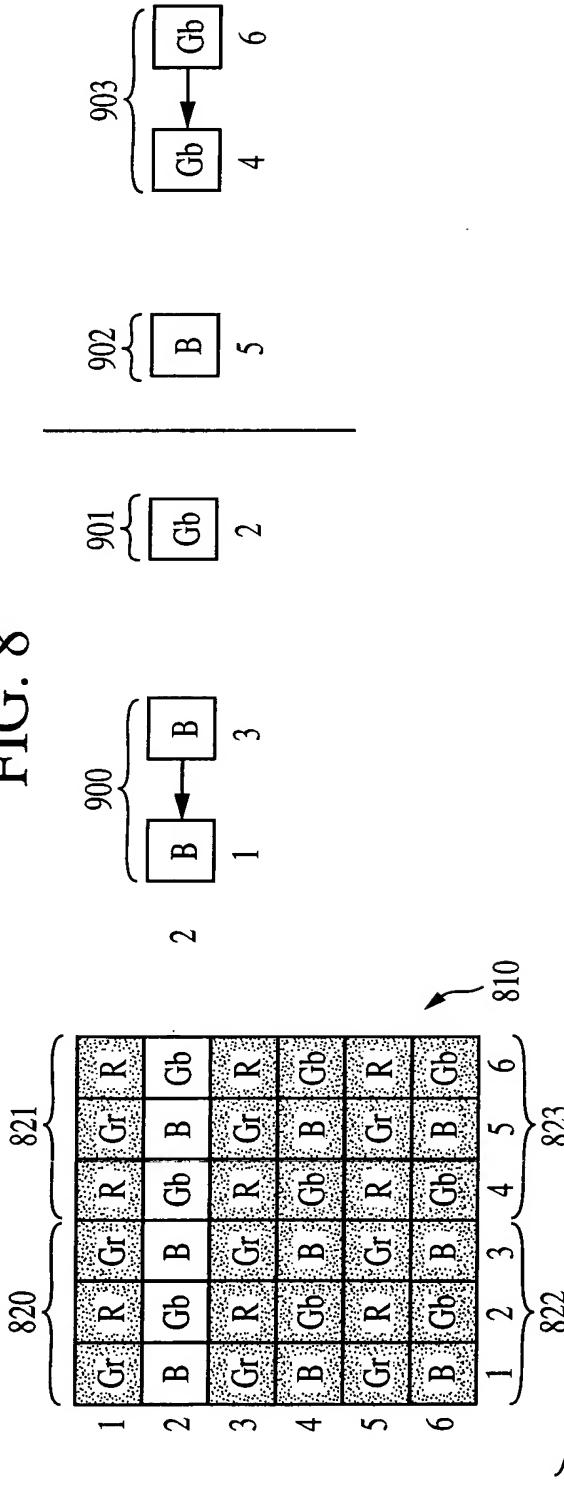


FIG. 9

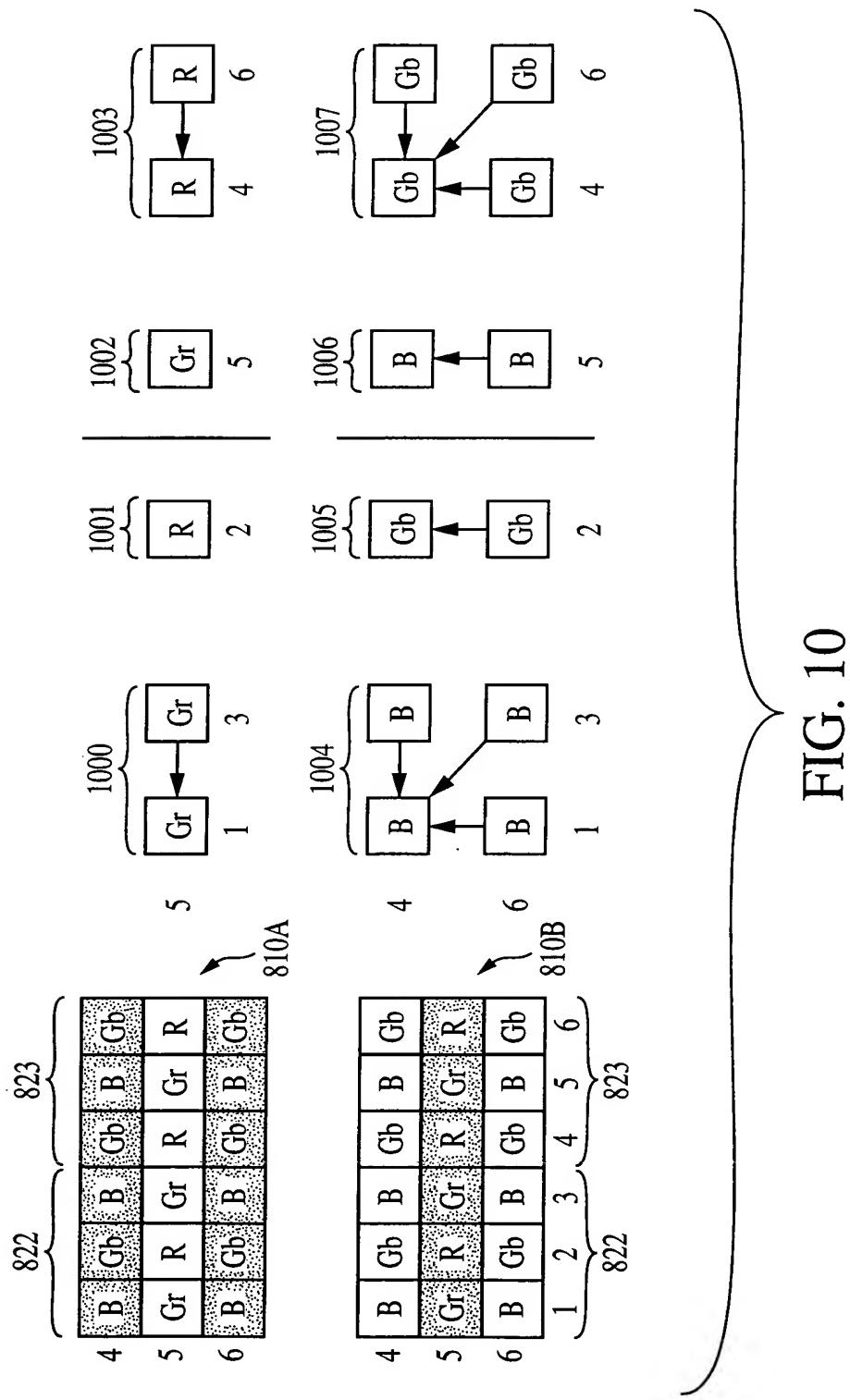


FIG. 10

1	Gr	R	Gr	R	Gr	R
2	B	Gb	B	Gb	B	Gb
3	Gr	R	Gr	R	Gr	R
4	B	Gb	B	Gb	B	Gb
5	Gr	R	Gr	R	Gr	R
6	B	Gb	B	Gb	B	Gb

1 2 3 4 5 6

FIG. 11

1	Gr	R	Gr	R	Gr	R
2	B	Gb	B	Gb	B	Gb
3	Gr	R	Gr	R	Gr	R
4	B	Gb	B	Gb	B	Gb
5	Gr	R	Gr	R	Gr	R
6	B	Gb	B	Gb	B	Gb

1 2 3 4 5 6

FIG. 12

1	Gr	R	Gr	R	Gr	R
2	B	Gb	B	Gb	B	Gb
3	Gr	R	Gr	R	Gr	R
4	B	Gb	B	Gb	B	Gb
5	Gr	R	Gr	R	Gr	R
6	B	Gb	B	Gb	B	Gb

1 2 3 4 5 6

FIG. 13

1	Gr	R	Gr	R	Gr	R
2	B	Gb	B	Gb	B	Gb
3	Gr	R	Gr	R	Gr	R
4	B	Gb	B	Gb	B	Gb
5	Gr	R	Gr	R	Gr	R
6	B	Gb	B	Gb	B	Gb

1 2 3 4 5 6

FIG. 14

FIG. 15A

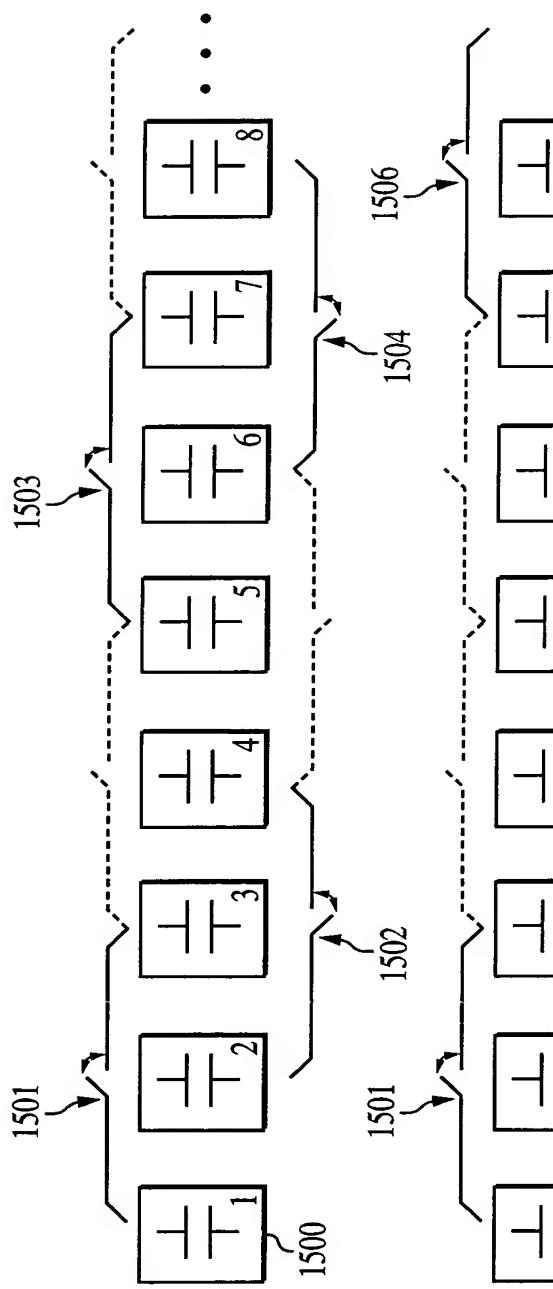


FIG. 15B

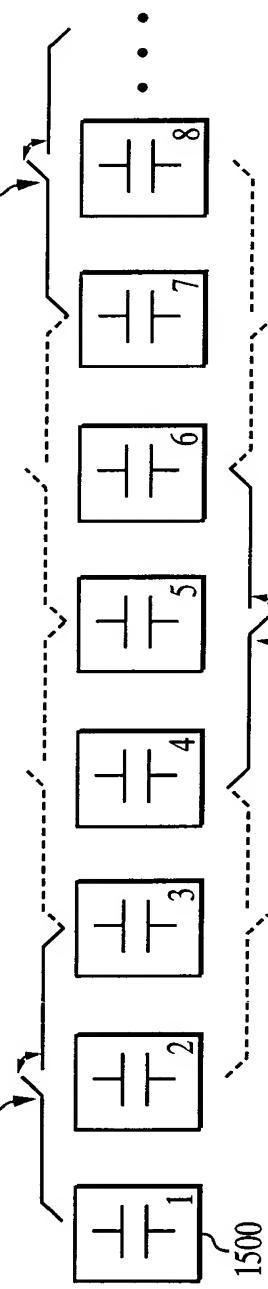
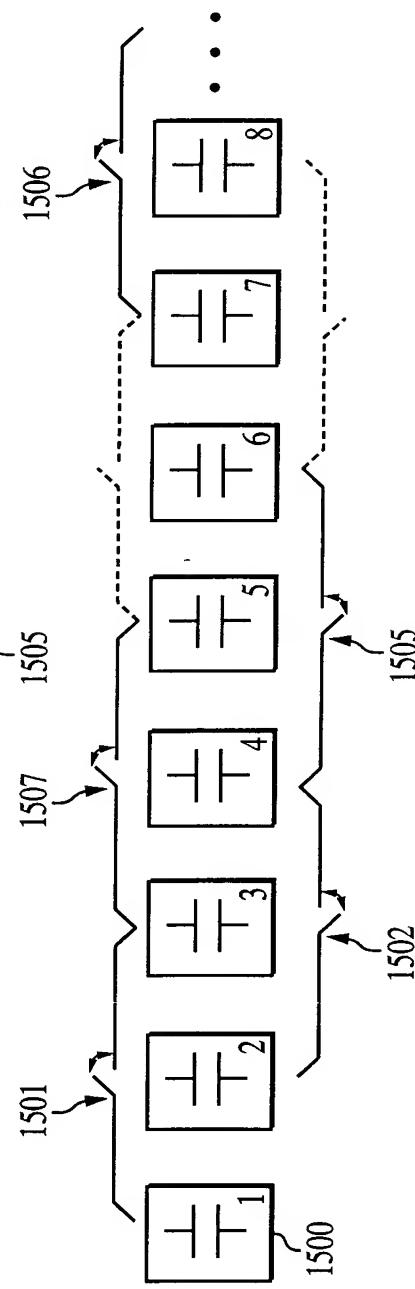


FIG. 15C



App No.: 10/705,843
Inventor: Alexander Krymski
Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN
COLUMN CIRCUITS OF A SENSOR CIRCUIT
REPLACEMENT SHEET

Docket No.: M4065.0960/P960

Cy	Ye	Cy	Ye
G	Mg	G	Mg
Cy	Ye	Cy	Ye
Mg	G	Mg	G

1600

FIG. 16

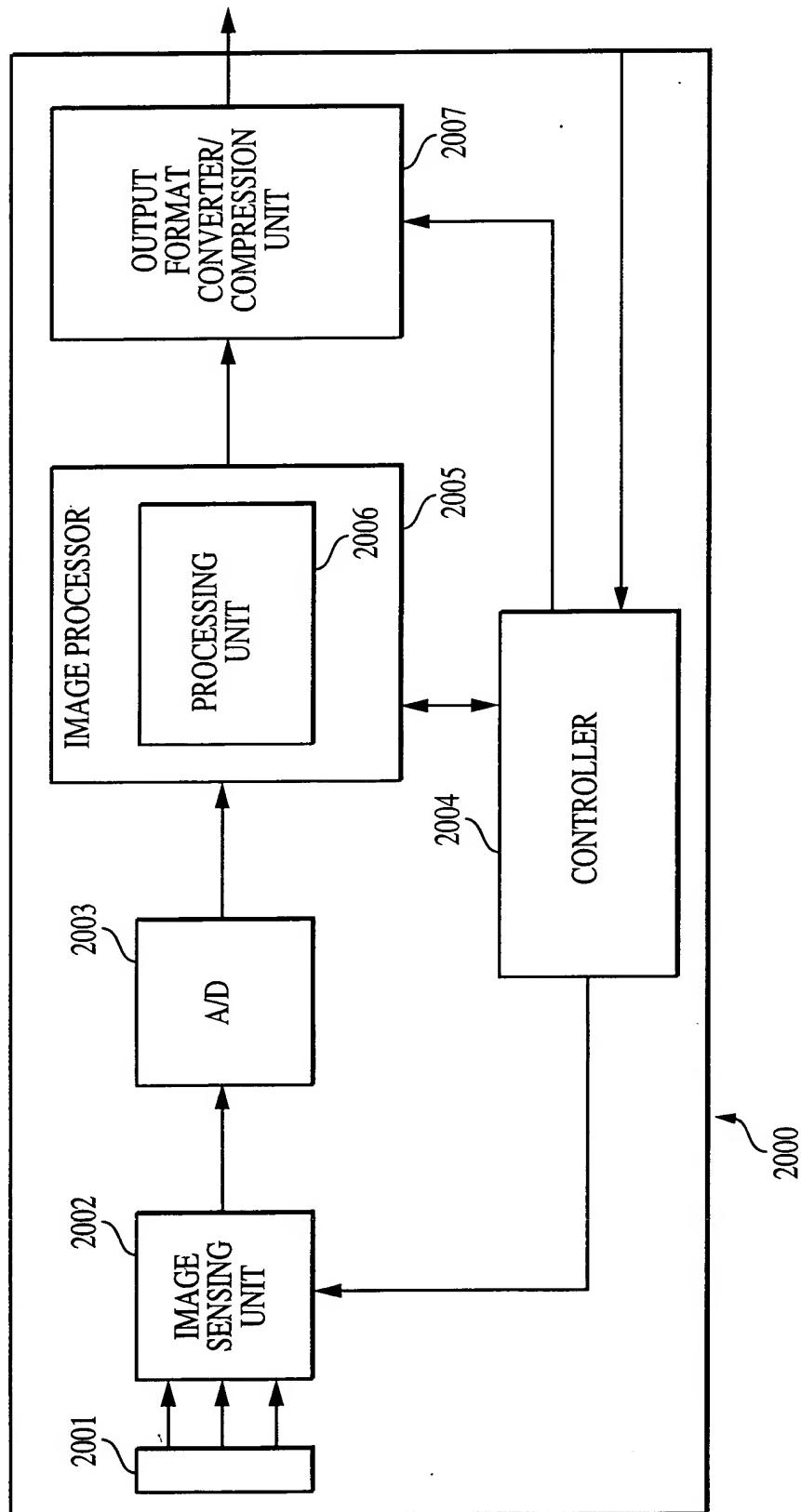


FIG. 17

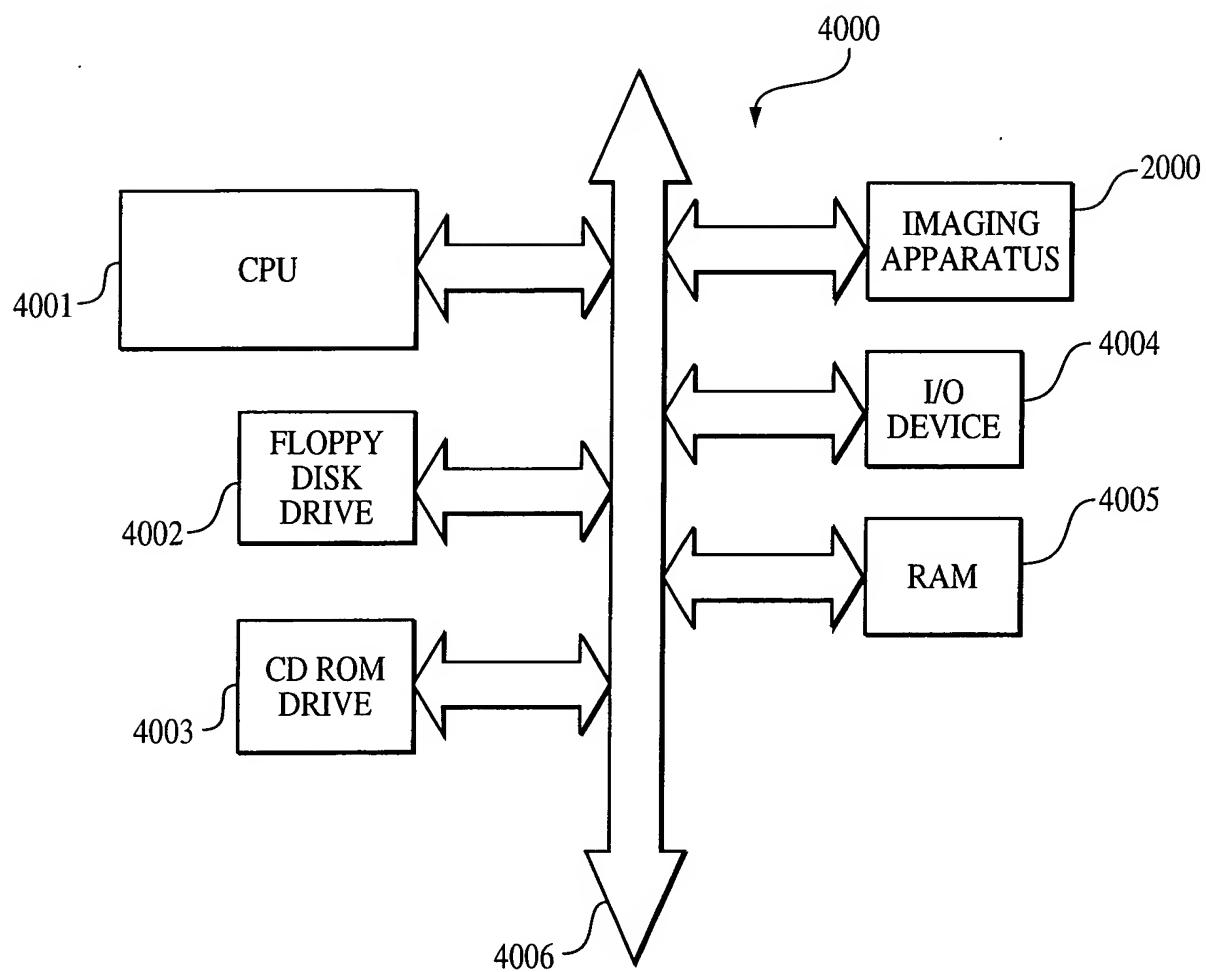


FIG. 18